

DEC 10 2007

PATENT APPLICATION
DOCKET NO.: 200209001-1

AMENDMENTS TO THE SPECIFICATION

Please replace Paragraph [0002] of the specification with the following amended paragraph:

[0002] This application is related to U.S. Patent Application Serial No. 10/635,371, entitled No. _____, filed _____ ~~entitled COVERAGE CIRCUIT FOR PERFORMANCE COUNTER; (Docket No. 200208996-1);~~ U.S. Patent Application Serial No. 10/635,372, entitled No. _____, filed _____ entitled COVERAGE DECODER CIRCUIT FOR PERFORMANCE COUNTER; ~~(Docket No. 200208997-1);~~ U.S. Patent Application Serial No. 10/635,103, entitled No. _____, filed _____ entitled DATA SELECTION CIRCUIT FOR PERFORMANCE COUNTER; ~~(Docket No. 200209000-1);~~ U.S. Patent Application Serial No. 10/635,083, entitled No. _____, filed _____ entitled GENERAL PURPOSE PERFORMANCE COUNTER; ~~(Docket No. 200208999-2);~~ U.S. Patent Application Serial No. 10/635,373, entitled No. _____, filed _____ entitled MATCH CIRCUIT FOR PERFORMANCE COUNTER; ~~(Docket No. 200209002-1);~~ and U.S. Patent Application

PATENT APPLICATION
DOCKET NO.: 200209001-1

Serial No. 10/635,369, entitled No. _____, filed _____
_____ entitled INCREMENT/DECREMENT CIRCUIT FOR PERFORMANCE COUNTER,
(Docket No. 200208998-1), all of which were filed on August 6,
2003 and which are hereby incorporated by reference in their
entirety.

PATENT APPLICATION
DOCKET NO.: 200209001-1

Please replace Paragraphs [0021] and [0022] of the specification with the following amended paragraphs:

[0021] Additional details regarding operation of the match/threshold circuit 202 are provided in U.S. Patent Application Serial No. 10/635,373, filed August 6, 2003, entitled No. _____, filed _____ entitled MATCH CIRCUIT FOR PERFORMANCE COUNTER. ~~(Docket No. 200209002-1).~~

[0022] The sm_sel circuit 204 selects an N -bit portion of the debug_bus signal aligned on a selected 10-bit block boundary into both the match portion 300 and the threshold portion 302 (FIG. 3) of the match/threshold circuit 202 and to a sum input of the counter circuit 208. As previously stated, in the illustrated embodiment, N is equal to 16. Additional details regarding the operation of the sm_sel circuit 204 are provided in U.S. Patent Application Serial No. 10/635,103, filed August 6, 2003, entitled No. _____, filed _____ entitled DATA SELECTION CIRCUIT FOR PERFORMANCE COUNTER. ~~(Docket No. 200209000-1).~~